# MOS FIELD EFFECT TRANSISTOR 2SK1588

# N-CHANNEL MOSFET FOR SWITCHING

#### DESCRIPTION

NEC

The 2SK1588 is an N-channel vertical type MOSFET which can be driven by 2.5 V power supply.

As the MOSFET is driven by low voltage and does not require consideration of driving current, it is suitable for appliances including VCR cameras and headphone stereos which need power saving.

### FEATURES

- Directly driven by ICs having a 3 V power supply.
- Low on-state resistance

 $R_{DS(on)1} = 0.5 \Omega MAX. (V_{GS} = 2.5 V, I_D = 1.0 A)$ 

 $R_{DS(on)2}$  = 0.3  $\Omega$  MAX. (V<sub>GS</sub> = 4.0 V, I<sub>D</sub> = 1.5 A)

#### ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK1588	SC-62 (Power Mini Mold)

#### Marking: NG

#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

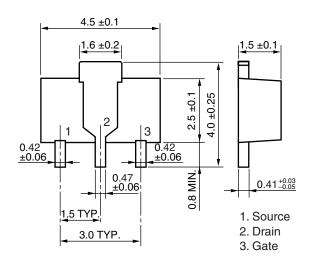
Drain to Source Voltage (Vgs = 0 V)	VDSS	16	V	
Gate to Source Voltage (VDS = 0 V)	Vgss	±16	V	
Drain Current (DC)	D(DC)	±3.0	Α	
Drain Current (pulse) <sup>Note1</sup>	D(pulse)	±6.0	Α	
Total Power Dissipation Note2	Рт	2.0	W	
Channel Temperature	Tch	150	°C	
Storage Temperature	Tstg	–55 to +150	°C	

**Notes 1.** PW  $\leq$  10 ms, Duty Cycle  $\leq$  50%

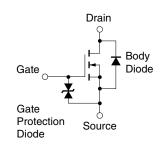
- 2. Mounted on ceramic substrate of 16 cm<sup>2</sup> x 0.7 mm
- **Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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#### PACKAGE DRAWING (Unit: mm)



#### EQUIVALENT CIRCUIT

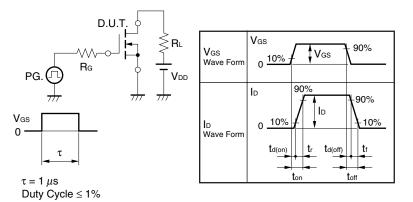


## ELECTRICAL CHARACTERISTICS (TA = 25°C)

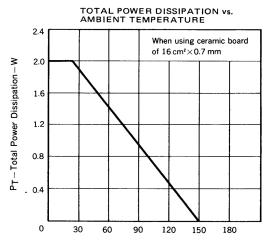
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 16 V, V <sub>GS</sub> = 0 V			1.0	μA
Gate Leakage Current	lgss	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V			±5.0	μA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 5.0 V, I <sub>D</sub> = 1.0 mA	0.8	1.0	1.6	V
Forward Transfer Admittance Note	y <sub>fs</sub>	V <sub>DS</sub> = 3.0 V, I <sub>D</sub> = 1.0 A	0.4	3.0		S
Drain to Source On-state Resistance Note	RDS(on)1	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 1.0 A		0.25	0.5	Ω
	RDS(on)2	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 1.5 A		0.17	0.3	Ω
Input Capacitance	Ciss	V <sub>DS</sub> = 3.0 V		240		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		250		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		60		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 3.0 V, I <sub>D</sub> = 1.5 A		140		ns
Rise Time	tr	V <sub>GS</sub> = 3 V		650		ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 10 Ω		120		ns
Fall Time	tr			160		ns

Note Pulsed

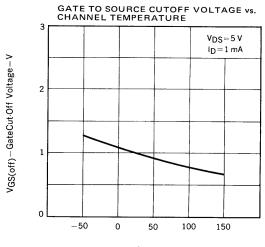
#### TEST CIRCUIT SWITCHING TIME



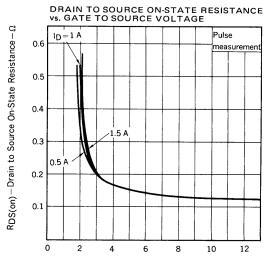
TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)



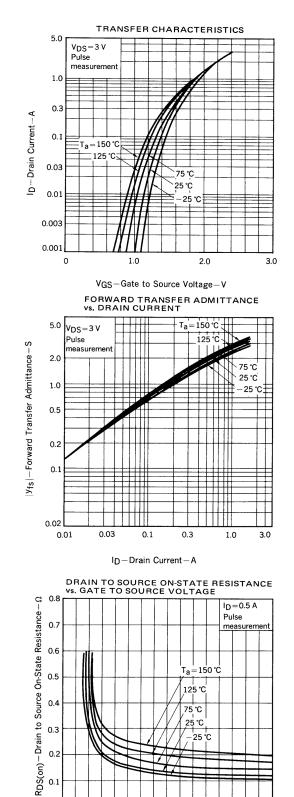
Ta-Ambient Temperature-°C



Tch-Channel Temperature-°C



VGS-Gate to Source Voltage-V

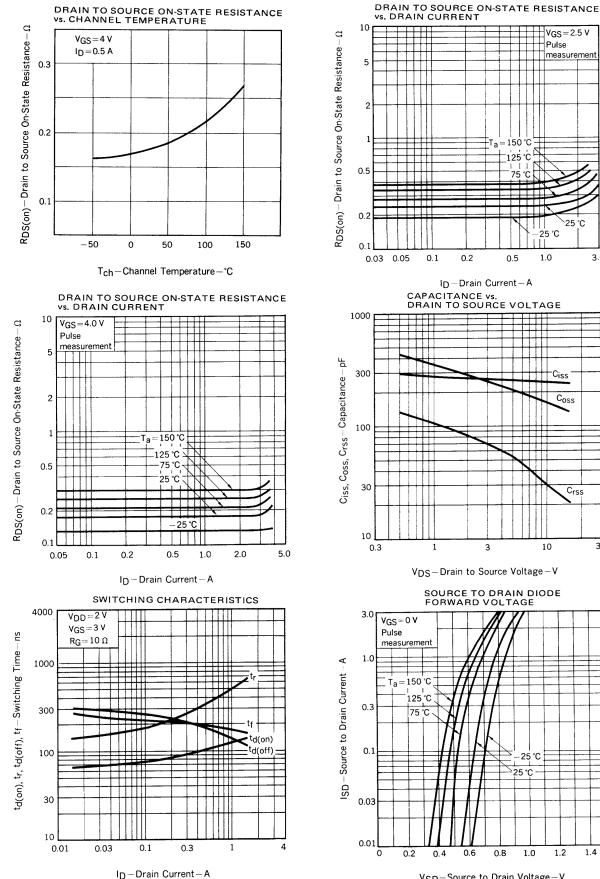


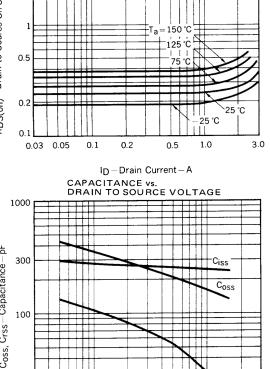
0 2 4 6 8 10 12 14 16 VGS-Gate to Source Voltage-V

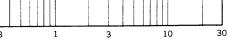
VGS=2.5 V

measurement

Pulse

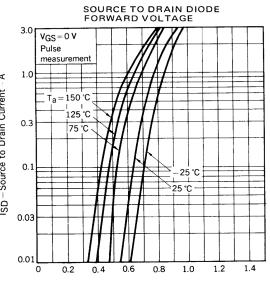






Crss





V<sub>SD</sub>-Source to Drain Voltage-V

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